

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions, and listing of claims in the application:

LISTING OF CLAIMS:

1. (Currently amended) A synchronous rectification circuit with dead time regulation connected to a secondary side of a transformer, an inductor, a first switch and a second switch for producing DC power, said circuit comprising:

a first switch control circuit connected to one terminal on the secondary side of said transformer and to said first switch, said first switch control circuit ~~receiving an input voltage from the secondary side of said transformer to control ON/OFF of said first switch~~ including (a) a waveform shaping circuit connected to one terminal on the secondary side of said transformer, said waveform shaping circuit shaping an input voltage to output a first driving voltage, and (b) a first driver circuit connected to said waveform shaping circuit and to said first switch, said first driver circuit receiving said first driving voltage and producing a first control signal to control ON/OFF of said first switch;

a dead time regulation circuit connected to said DC power and to said first ~~switch control~~ driver circuit, said dead time regulation circuit receiving

said first control signal and producing a dead time regulation signal;

and

a second switch control circuit connected to one terminal on the secondary side of said transformer, to said second switch and to said dead time regulation circuit, said second switch control circuit receiving ~~an~~ said input voltage from the secondary ~~said~~ side of said transformer and said dead time regulation signal to control ON/OFF of said second switch.

2. (Cancelled).

3. (Original) The synchronous rectification circuit with dead time regulation of claim 1, wherein said dead time regulation circuit comprises:

a pulse generator circuit connected to said first switch control circuit, said pulse generator circuit producing a pulse signal based on said first control signal output from said first switch control circuit;

a voltage regulation circuit connected to said DC power, said voltage regulation circuit producing charging power;

a charging circuit connected to said pulse generator circuit and to said voltage regulation circuit, said charging circuit performing a charge/discharge operation based on said charging power and said pulse signal to produce a dead time regulation comparison signal; and

a first comparator circuit connected to a first reference voltage level, to said charging circuit, to said pulse generator circuit and to said second switch control circuit, said first comparator circuit comparing said first reference voltage level with said dead time regulation comparison signal to produce said dead time regulation signal.

4. (Original) The synchronous rectification circuit with dead time regulation of claim 3, wherein said dead time regulation comparison signal is a sawtooth wave signal.

5. (Original) The synchronous rectification circuit with dead time regulation of claim 3, wherein said charging circuit is an RC charging circuit.

6. (Original) The synchronous rectification circuit with dead time regulation of claim 1, wherein said second switch control circuit comprises:

an inverter circuit connected to one terminal on the secondary side of said transformer, said inverter circuit inverting said input voltage to output an inverted voltage;

a logic circuit connected to said inverter circuit and to said dead time regulation circuit, said logic circuit performing a logic AND operation

on said inverted voltage and said dead time regulation signal to produce
a second driving voltage; and

a second driver circuit connected to said logic circuit and to said second
switch, said second driver circuit receiving said second driving voltage
and producing a second control signal to control ON/OFF of said
second switch.

7. (Original) The synchronous rectification circuit with dead time regulation
of claim 1, further comprising a low voltage protection circuit connected to said
DC power, to said first switch control circuit and to said second switch control
circuit, said low voltage protection circuit receiving said input voltage and
controlling operation of said first switch control circuit and said second switch
control circuit.

8. (Original) The synchronous rectification circuit with dead time regulation
of claim 7, wherein said low voltage protection circuit comprises:

a second comparator circuit connected to said DC power, said second
comparator circuit comparing said DC power with a second reference
voltage level to produce a protection control signal;
an electronic switch circuit connected to said second comparator circuit, to
said first switch control circuit and to said second switch control circuit,

said electronic switch circuit receiving said protection control signal and being controlled to ground output voltages of said first switch control circuit and said second switch control circuit.

9. (Original) The synchronous rectification circuit with dead time regulation of claim 8, wherein said electronic switch circuit comprises a transistor connected with at least one diode.

10. (Currently amended) The synchronous rectification circuit with dead time regulation of claim ~~[[1]]~~ 7, wherein said first switch control circuit, said second switch control circuit, said dead time regulation circuit and said low voltage protection circuit ~~of claim 6~~ are packaged as a control IC having at least a power ~~pin~~ terminal (Vc), a ground ~~pin~~ terminal (Gnd), a first input ~~pin~~ terminal (Vin1), a second input ~~pin~~ terminal (Vin2), a first output ~~pin~~ terminal (Gate1), a second output ~~pin~~ terminal (Gate2), a reference voltage ~~pin~~ terminal (VREF) and a reference capacitor ~~pin~~ terminal (RCT).